		<b>—</b>		
-	785	713/193.ccls.	USPAT; US-PGPUB;	2004/08/08 15:19
			EPO; JPO; DERWENT;	
_	428	327/545.ccls.	IBM_TDB USPAT; US-PGPUB;	2004/08/08 15:19
,			EPO; JPO; DERWENT; IBM TDB	
-	931	711/163.ccls.	USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:27
	9961	(EDC) HEigld Degreemable gate every	DERWENT; IBM_TDB USPAT;	2004/08/08 18:05
-	9961	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" ) and ((integrated adj circuits) IC)	US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 18:05
-	173386	(key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/08 18:05
-	664	((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" ) and ((integrated adj circuits) IC)) and	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/08 15:38
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature)	DERWENT; IBM_TDB	
_	6640475	(first one primary 1st) and (second\$3 two 2nd alternative)	USPAT; US-PGPUB; EPO; JPO;	2004/08/08 18:06
_	4	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays")	DERWENT; IBM_TDB USPAT; US-PGPUB;	2004/08/08 15:45
		and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	EPO; JPO; DERWENT; IBM_TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd		
	14 -	alternative))) and 711/163.ccls.		
	138	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" ) and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/08 15:45
		\$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one	IBM_TDB	
-	0	primary 1st) and (second\$3 two 2nd alternative))) and "111" ((((FPGA "Field Programable gate array"))	USPAT; US-PGPUB;	2004/08/08 15:45
		FPGAs "Field Programable gate arrays" ) and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC	DS-PGPOB; EPO; JPO; DERWENT; IBM TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one		
		primary 1st) and (second\$3 two 2nd alternative))) and 327/545.ccls.		
	·		L	

-	6	((((FPGA "Field Programable gate array"	USPAT;	2004/08/08 15:48
		FPGAs "Field Programable gate arrays" )	US-PGPUB;	
		and ((integrated adj circuits) IC)) and	EPO; JPO;	
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3	DERWENT;	
		\$2compress\$3 \$2scrambl\$3)) and (CRC	IBM_TDB	
		"cyclic Redundancy Check" checksum MAC "message authentication code" signature)		
		and (pseudo random)) and ((first one		
		primary 1st) and (second\$3 two 2nd		:
		alternative))) and 713/193.ccls.		
1_	1074	713/2.ccls.	USPAT;	2004/08/08 15:49
	10,1	71372.0013.	US-PGPUB;	2004/00/00 15:45
ļ			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	787	713/100.ccls.	USPAT;	2004/08/08 15:49
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/65/55
-	0	713/1190.ccls.	USPAT;	2004/08/08 15:49
			US-PGPUB;	
			EPO; JPO;	
1			DERWENT; IBM TDB	
1_	160	713/190.ccls.	USPAT;	2004/08/08 15:49
_	160	/13/190.CC13.	US-PGPUB;	2004/00/00 13:49
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	· 785	713/193.ccls.	USPAT;	2004/08/08 15:49
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	239	713/194.ccls.	USPAT;	2004/08/08 15:49
			US-PGPUB;	
1			EPO; JPO;	
			DERWENT;	
_	171	713/187.ccls.	IBM_TDB USPAT;	2004/08/08 15:49
	1/1	7137107.0013.	US-PGPUB;	2004/00/00 15:43
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	3	((((FPGA "Field Programable gate array"	USPAT;	2004/08/08 15:50
		FPGAs "Field Programable gate arrays" )	US-PGPUB;	
		and ((integrated adj circuits) IC)) and	EPO; JPO;	
1		((key (mask)) and (\$2crypt\$3 \$2cipher\$3	DERWENT;	
		\$2compress\$3 \$2scrambl\$3)) and (CRC	IBM_TDB	
		"cyclic Redundancy Check" checksum MAC		
1		"message authentication code" signature)		
		and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd		
		alternative))) and 713/2.ccls.		
_	2		USPAT;	2004/08/08 15:52
1	_	FPGAs "Field Programable gate arrays" )	US-PGPUB;	
1		and ((integrated adj circuits) IC)) and	EPO; JPO;	
		((key (mask)) and (\$2crypt\$3 \$2cipher\$3	DERWENT;	
1		\$2compress\$3 \$2scramb1\$3)) and (CRC	IBM TDB	
		"cyclic Redundancy Check" checksum MAC	_	
		"message authentication code" signature)		
		and (pseudo random)) and ((first one		
		primary 1st) and (second\$3 two 2nd		
		alternative))) and 713/100.ccls.		1

-	0	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" ) and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/08 15:52
		\$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/1190.ccls.	IBM_TDB	
-	1	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52
_	6	alternative))) and 713/187.ccls.  ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:55
_	5	alternative))) and 713/190.ccls.  ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:53
-	6	alternative))) and 713/194.ccls. ((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))) and 713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:55
-	484	(((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second\$3 two 2nd alternative))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:56
_	7	l	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:06
_	4	((FPGA with chip) and (memory storage "hard drive") and ("Field Programable Interconnect Components" FPICs)) and ((\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:05

_	127	(FPGA with chip) and (memory storage "hard drive") and (((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:43
-	2	09/780681	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:39
-	1	(FPGA with chip) and (contact adj layer) and (polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 18:07
_	179	(polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:43
-	1	(polysilicon adj wire) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:45
_	489	(polysilicon ) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:45
_	1165	(SRAM "Static Random Access Memory") with FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 16:57
-	79	((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and ((SRAM "Static Random Access Memory") with FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:57
_	965	"53" and CBC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:19
_	2	("6356637").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:36
_	8	(("6324286") or ("4878246") or ("6615349") or ("5036468")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/08 17:38
-	2	("6357037") PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:05

		•		
<u> </u>	2	(FPGA "Field Programable gate array"	USPAT;	2004/08/08 18:07
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	İ
		checksum MAC "message authentication code"	_	
		signature first one primary 1st second\$3		1
		two 2nd alternative) and (("6357037").PN.)		
_	2		USPAT;	2004/08/09 04:49
		FPGAs "Field Programable gate arrays"	US-PGPUB;	=====================================
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"	1511_155	
		signature first one primary 1st second\$3		
		two 2nd alternative FPGA chip contact		
		layer polysilicon adj wire) and		
	1000	(("6357037").PN.)		0004/00/00 10 10
-	1262	burnham-\$-\$.in.	USPAT;	2004/08/08 18:12
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	18	burnham-\$-\$.in. and FPGA	USPAT;	2004/08/08 18:12
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	18	(FPGA "Field Programable gate array"	USPAT;	2004/08/08 23:33
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
		checksum MAC "message authentication code"	1011_100	
		signature first one primary 1st second\$3	]	
		two 2nd alternative FPGA chip contact	•	
		·		
		layer polysilicon adj wire) and		
		(burnham-\$-\$.in. and FPGA)	HODAM.	2004/08/08 18:29
-	0		USPAT;	2004/08/08 18:29
		FPGAs "Field Programable gate arrays"	US-PGPUB;	
		(integrated adj circuits) IC key mask	EPO; JPO;	
		\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
		\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM_TDB	
		checksum MAC "message authentication code"		
		signature first one primary 1st second\$3		
		two 2nd alternative FPGA chip contact		
		layer polysilicon adj wire) and		
		(burnham-\$-\$.in. and FPGA) and mask		
_	78	713/100.ccls. and mask	USPAT;	2004/08/08 18:29
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	19	((US-6122716-\$ or US-5666516-\$ or	USPAT;	2004/08/08 18:29
		US-6324676-\$ or US-6640305-\$ or	US-PGPUB;	=====================================
		US-6633905-\$ or US-6408432-\$ or	EPO; JPO;	
		US-6324288-\$ or US-6279045-\$ or	DERWENT;	
		US-6215327-\$ or US-6151677-\$ or	IBM TDB	
		US-6147890-\$ or US-6134707-\$ or	1201-122	
		US-5717394-\$ or US-5687325-\$ or	1	
		US-6772387-\$ or US-6560743-\$ or		
		US-6357037-\$ or US-6118869-\$ or	1	
				1
		US-6049222-\$).did. or (US-20040111631-\$ or	1	
		US-20020099948-\$ or US-20020026539-\$ or		
		US-20010032318-\$ or US-20010015919-\$ or	1	
	1	US-20010037482-\$).did.) and mask		1

-	0	713.200.ccls.	USPAT;	2004/08/09 00:20
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	380.200.ccls.	USPAT;	2004/08/09 00:20
	1		US-PGPUB; EPO; JPO;	
İ			DERWENT;	
			IBM_TDB	
-	0	380.30.ccls.	USPAT;	2004/08/09 00:21
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	53	714/100.ccls.	USPAT;	2004/08/09 00:22
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	371	717/124.ccls.	USPAT;	2004/08/09 00:23
			US-PGPUB;	
			EPO; JPO; DERWENT;	
	1		IBM_TDB	
-	180	717/139.ccls.	USPĀT;	2004/08/09 00:22
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
-	6	717/139.ccls. and FPGA	USPAT;	2004/08/09 00:22
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
-	10	Kelem-\$-\$.in. and FPGA	USPĀT;	2004/08/09 01:02
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
_	48	(xilinx same FPGA) and mask and	USPAT;	2004/08/09 01:16
		(distribut\$3 provider) and multiple and bitstream and (\$2crypt\$3 \$2cipher\$3	US-PGPUB; EPO; JPO;	
		\$2compress\$3 \$2scrambl\$3)	DERWENT;	
			IBM TDB	
_	56	(xilinx same FPGA) and mask and multiple	USPAT;	2004/08/09 01:18
		and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)	US-PGPUB; EPO; JPO;	
		1-10	DERWENT;	
			IBM_TDB	
_	8	((xilinx same FPGA) and mask and multiple	USPAT;	2004/08/09 01:16
		and bitstream and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3 \$2scrambl\$3)) not ((xilinx	US-PGPUB; EPO; JPO;	
		same FPGA) and mask and (distribut\$3	DERWENT;	
		provider) and multiple and bitstream and	IBM_TDB	
		(\$2crypt\$3 \$2cipher\$3 \$2compress\$3   \$2scrambl\$3))		
-	56		USPAT;	   2004/08/09 01:18
		and (\$2crypt\$3 \$2cipher\$3 \$2compress\$3	US-PGPUB;	
		\$2scramb1\$3)	EPO; JPO;	
	1		DERWENT;	
-	132	(xilinx same FPGA) and mask and (\$2crypt\$3	IBM_TDB USPAT;	   2004/08/09 01:18
1		\$2cipher\$3 \$2compress\$3 \$2scrambl\$3)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
-	76	((xilinx same FPGA) and mask and	IBM_TDB USPAT;	2004/08/09 01:19
		(\$2crypt\$3 \$2cipher\$3 \$2compress\$3	US-PGPUB;	
		\$2scrambl\$3)) not ((xilinx same FPGA) and	EPO; JPO;	
		mask and bitstream and (\$2crypt\$3	DERWENT;	
L		\$2cipher\$3 \$2compress\$3 \$2scramb1\$3))	IBM_TDB_	<u> </u>

 2	(FPGA "Field Programable gate array"	USPAT;	2004/08/09 03:50
	FPGAs "Field Programable gate arrays"	US-PGPUB;	
	(integrated adj circuits) IC key mask	EPO; JPO;	
	\$2crypt\$3 \$2cipher\$3 \$2compress\$3	DERWENT;	
	\$2scrambl\$3 CRC "cyclic Redundancy Check"	IBM TDB	
	checksum MAC "message authentication code"	_	
	signature first one primary 1st second\$3		
	two 2nd alternative FPGA chip contact		
	layer polysilicon adj wire) and		
	(("6061449").PN.)		